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REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2133

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,708
Filed: October 12, 2001
Inventor(s):
Michael C. Dorsey

§ Examiner: Trimmings, J.
§ Group/Art Unit: 2133
§ Atty. Dkt. No: 5681-55500

Title: ASIC LOGIC BIST
EMPLOYING REGISTERS
SEEDED WITH
DIFFERING PRIMITIVE
POLYNOMIALS

§ I hereby certify that this correspondence is being deposited with
§ the United States Postal Service with sufficient postage as first
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§ Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the
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	Erik A. Heter
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Signature	Date

AMENDMENT; RESPONSE TO OFFICE ACTION OF
October 29, 2004

ATTN: BOX AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of October 29, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below. A listing of the claims, including any amendments, begins on page 2. Remarks begin on page 9.